A Vector Type Accelerator for High Performance Computing

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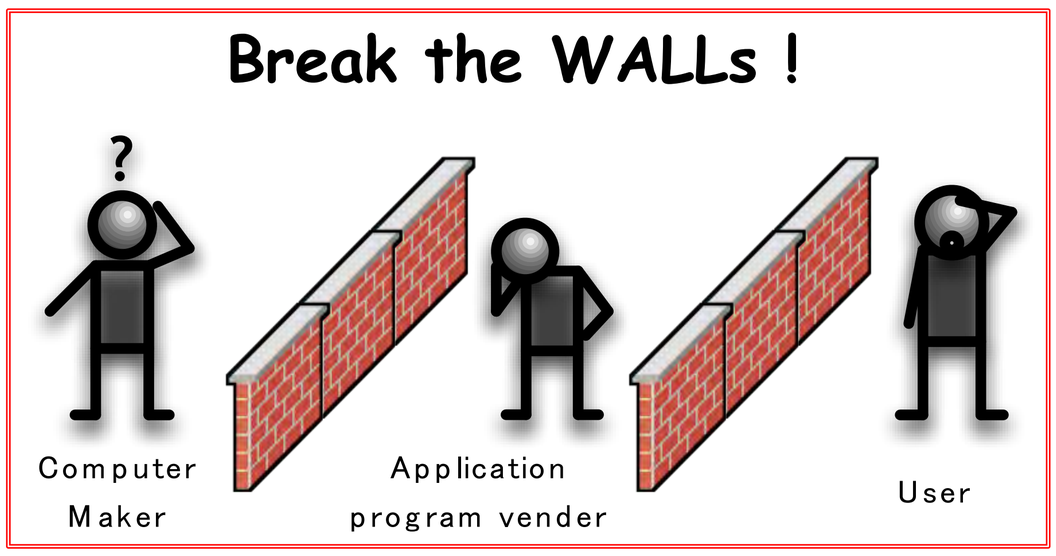
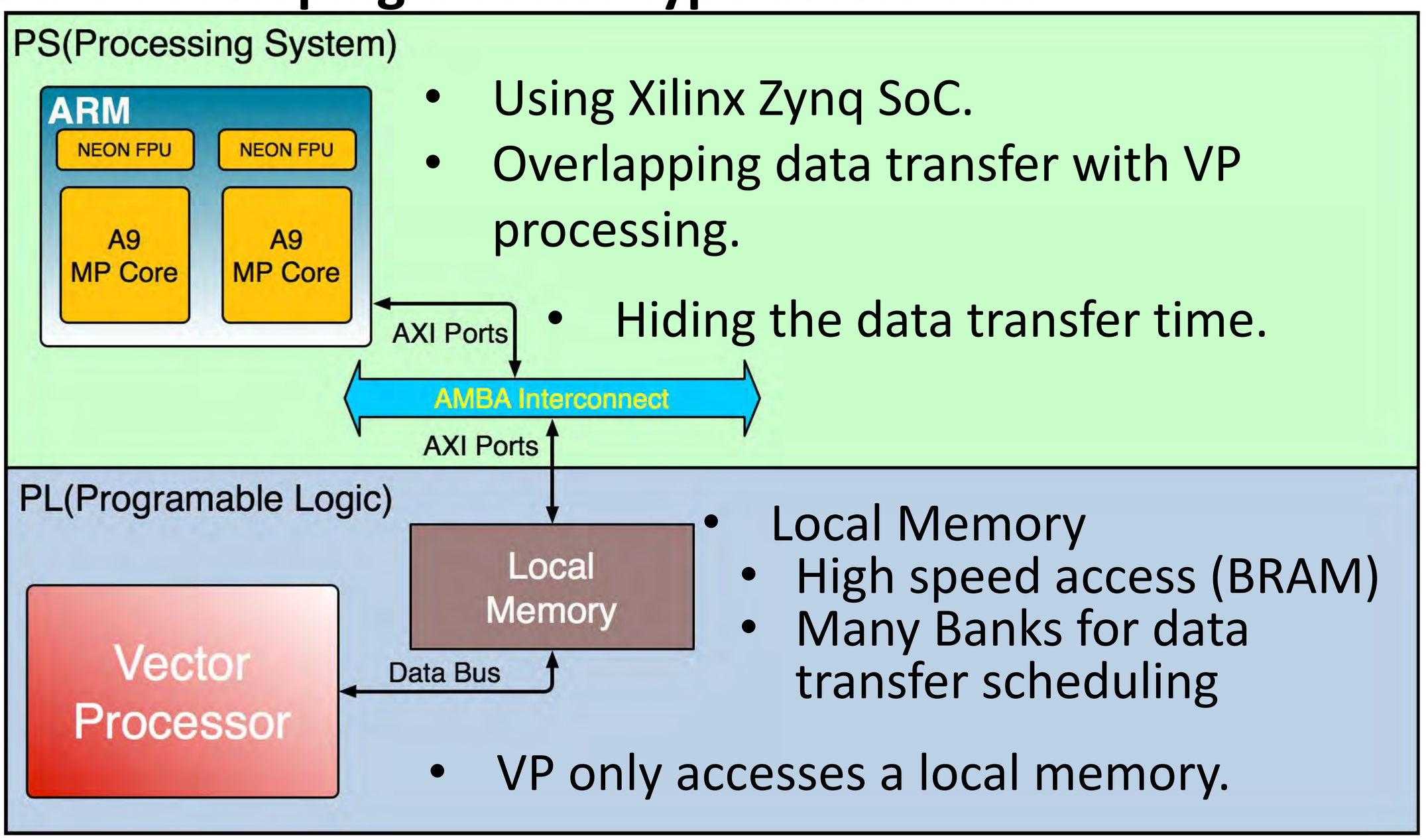
We propose New CAE Environment:

- User friendly and easy-to-use packages.
- Add-on hardware Accelerators or tailored Servers for these packages.
- No hassle optimization required not only Users but also Package vender.



We are developing a vector type accelerator



- Using Xilinx Zynq SoC.
- Overlapping data transfer with VP processing.
- Hiding the data transfer time.
- Local Memory
  - High speed access (BRAM)
  - Many Banks for data transfer scheduling
- VP only accesses a local memory.

Future System Overview: