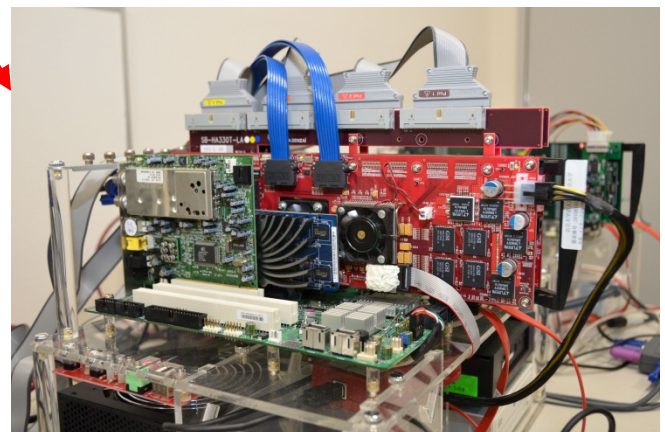
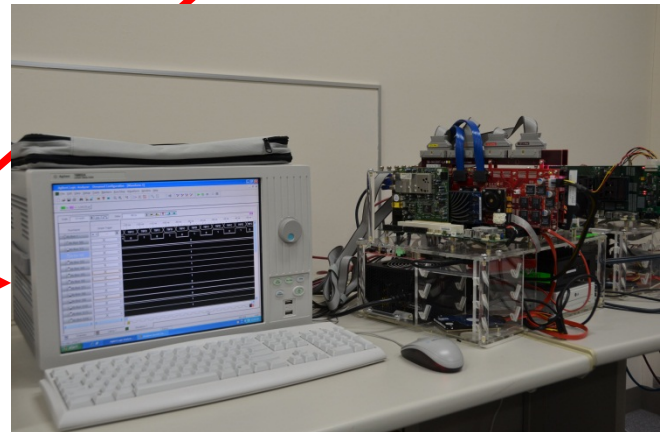
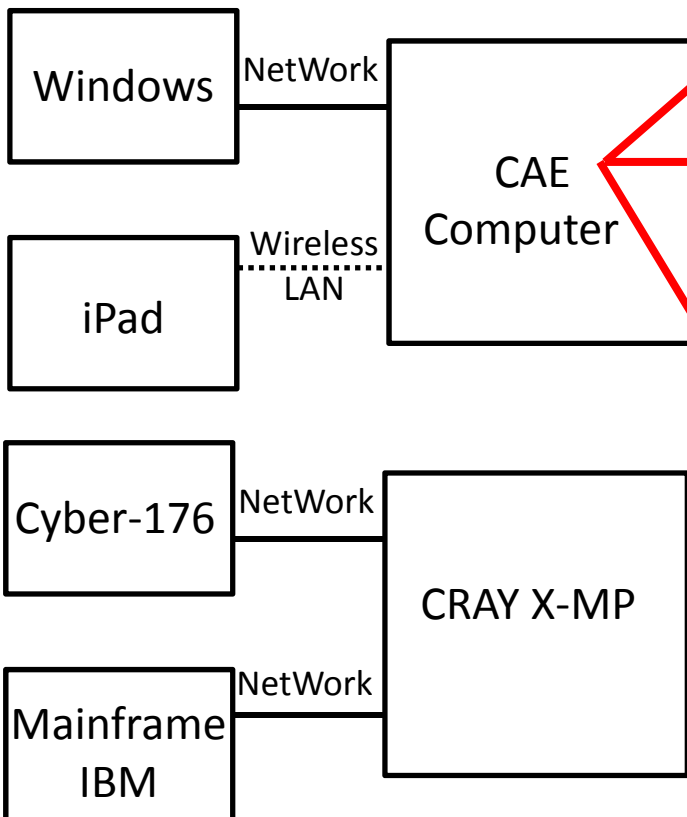


# Research Group on CAE Computer

## ★CAE Computer

- **Applications Pre-installed** for a Quick Ramp-up.
- Add-on to Current Windows/iPad Computer as a Back-End Accelerator.
- Speedy by far to Outperform Current Computer **30-60%** of Peak Performance of CAE Computer.
- **Low Power** Consumption and Low Electricity Charge.
- A Single Chip of a Multiple of Cores consisting of Scalar/Vector/Data-Transfer/Local-Memory, altogether interconnected by Crossbar.
- **Crossbar** is Easy to Allocate Cores and is Fast enough to Hide Communication behind Computation.



Experimental system is developed using a FPGA board.

System Configuration Back in Mid 80's

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 Toshiaki Kitamura (Hiroshima City University), Yoshinari Fukui (TUS)